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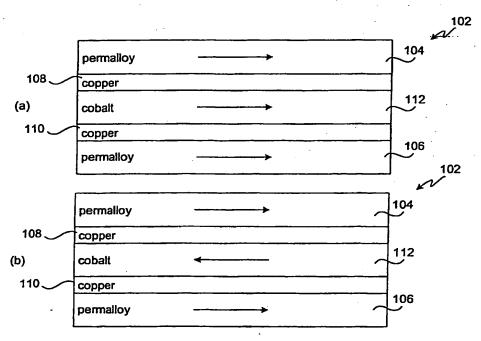
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(54) Title: MAGNETORESISTIVE TRIMMING OF GMR CIRCUITS



(57) Abstract: Methods and apparatus are described relating to an electronic device which includes at least one configurable resistive element (104, 106, 112). Each such configurable resistive element includes at least one multi-layer thin film (104, 106) element exhibiting giant magnetoresistance. The resistance value of each of configurable resistive element (104, 106) is configurable over a resistance value range by application of at least one magnetic field which manipulates at least one magnetization vector associated with the thin film element (104, 106).



INTERNATIONAL SEARCH REPORT

International application No. PCT/US01/20665

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B. FIELDS SEARCHED			
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C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category*	Citation of document, with indication, where ap	propriate, of the relevant passages	Relevant to claim No.
X	US 5,587,943 A (TOROK et al) 24 I	December 1996 (24.12.1996),	1-19
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